FORM PTO-1449	ATTY. DOCKET NO. 200302257-2	APPLICATION NO.	CONFIRMATION NO.

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT		
Kour sh GHARACHORLOO t al.		
FILING DATE	GROUP	
Herewith		

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
MC	1A	5,634,110	05-1997	Laudon et al.	
MC	1B	6,493,809	12-2002	Safranek et al.	
	1C				
	1D				
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

MC	1Q	Beng-Hong Lim et al., "Reactive Synchronization Algorithms for Multiprocessors," Proceedings of the Sixth International Conference on Architectural Support for Programming Languages and Operation Systems (ASPLOS VI), pages 25-35, October 1994.
MC	1R	Luiz A. Barroso et al., "Piranha: A Scalable Architecture Based on Single-Chip Multiprocessing," Copyright ACM 2000 (pages 282-293).
MC	1S	Brian N. Bershad et al., "Fast Mutual Exclusion for Uniprocessors," In Proceedings of 5th Conference on Architectural Support for Programming Languages and Operating Systems, October 1992, pages 223-233.

EXAMINER	DATE CONSIDERED
[signature]	07/08/06

Rev 05/03 (PTO1449)

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MC	1Q	J. Eliot et al., "Concurrency Features for the Trellis/Owl Language," In the proceedings of European conference on Object-Oriented Programming 1987 (ECOOP '87), Paris, France, June 1987, pages 171-180.
MC	1R	David A. Patterson, "Computer Architecture A Quantitative Approach," Second Edition, Published 1990, 2nd edition 1996, pages 679-685.
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EXAMINER *Murdvi Cwe Aez*	DATE CONSIDERED 09/08/06